

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3914	(queue near3 count) or (queue near3 size)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/09/27 18:47
L2	10156	queue near5 (instruction or command)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/27 18:58
L3	821	1 and 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/27 18:48
L4	280	1 same 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/27 18:48
L5	7995	count near3 field or count near3 fields	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/27 18:49
L6	36	4 and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/27 18:49
L7	12087419	@ad<"20011217"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/09/27 18:49
L8	32	6 and 7	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/09/27 18:54
L9	1	queue adj descriptor near2 count near2 instruction	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/09/27 18:55
L10	1961	711/154.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/09/27 18:56
L11	479	711/159.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/09/27 18:56
L12	539	711/108.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/09/27 18:56

L13	934	711/133.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/09/27 18:56
L14	1820	370/412.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/09/27 18:56
L15	427	709/216.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/09/27 18:56
L16	222	710/54.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/09/27 18:56
L17	6063	10 or 11 or 12 or 13 or 14 or 15 or 16	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/09/27 18:57
L18	3	6 and 17	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/09/27 18:57
L19	2	7 and 18	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/09/27 18:58
L20	677	711/154.ccls.	US-PGPUB	OR	OFF	2005/09/27 18:58
L21	167	7 and 20	US-PGPUB	OR	OFF	2005/09/27 18:58
L22	1867	queue.clm. near5 (instruction.clm. or command.clm.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/27 18:59
L23	7	21 and 22	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/27 18:59

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Select Article Information

- ☐ **1. Implementation of the PIPE processor**
Farrens, M.K.; Pleszhun, A.R.;
Computer
Volume 24, Issue 1, Jan. 1991 Page(s):65 - 70
Digital Object Identifier 10.1109/2.67195
[AbstractPlus](#) | Full Text: [PDF\(504 KB\)](#) IEEE JNL
- ☐ **2. Techniques to reduce the soft error rate of a high-performance microprocessor**
Weaver, C.; Emer, J.; Mukherjee, S.S.; Reinhardt, S.K.;
Computer Architecture, 2004. Proceedings. 31st Annual International Symposium on
19-23 June 2004 Page(s):264 - 275
Digital Object Identifier 10.1109/ISCA.2004.1310780
[AbstractPlus](#) | Full Text: [PDF\(372 KB\)](#) IEEE CNF
- ☐ **3. Defining wakeup width for efficient dynamic scheduling**
Aggarwal, A.; Franklin, M.; Ergin, O.;
Computer Design: VLSI in Computers and Processors, 2004. ICCD 2004. Proceedings. IEEE International Conference on
11-13 Oct. 2004 Page(s):36 - 41
Digital Object Identifier 10.1109/ICCD.2004.1347895
[AbstractPlus](#) | Full Text: [PDF\(315 KB\)](#) IEEE CNF
- ☐ **4. AccuPower: an accurate power estimation tool for superscalar microprocessors**
Ponomarev, D.; Kucuk, G.; Ghose, K.;
Design, Automation and Test in Europe Conference and Exhibition, 2002. Proceedings
4-8 March 2002 Page(s):124 - 129
Digital Object Identifier 10.1109/DATE.2002.998259
[AbstractPlus](#) | Full Text: [PDF\(231 KB\)](#) IEEE CNF
- ☐ **5. Transient fault detection via simultaneous multithreading**
Reinhardt, S.K.; Mukherjee, S.S.;
Computer Architecture, 2000. Proceedings of the 27th International Symposium on
2000 Page(s):25 - 36
[AbstractPlus](#) | Full Text: [PDF\(1412 KB\)](#) IEEE CNF
- ☐ **6. A comparison of structure accessing techniques in IdA and SISAL on the CSIRAC II dataflow multiprocessor**
Whiting, P.G.; Egan, G.K.;
Parallel and Distributed Processing, 1991. Proceedings of the Third IEEE Symposium on
2-5 Dec. 1991 Page(s):172 - 179
Digital Object Identifier 10.1109/SPDP.1991.218282
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